SELF-CONSISTENT MODELLING OF TRANSFERRED ELECTRON DEVICES

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A self-consistent modelling of the circuits consisting transferred electron device (TED) is proposed. The TED model is shown on the right hand side of Fig. 1. It is an improved form of the known models applied for non self-consistent modelling [1-5].



Fig. 1. The equivalent circuit of a TEO

The operating period without domain is represented by the position l of the switch S. In this case the RC_0 block represents the low field region of the TED only. The capacitance C_0 is assumed to be constant and the characteristics i_0 (u_0) of the nonlinear conductor is determined by the D.C. characteristics of the TED below threshold. [1-5] These parameters can be measured easily.

Position 2 of the switch S represents the operating period during which a domain is present in the TED. To describe the behaviour of the domain another RC_d block is connected in series with the RC_0 block representing the low-field region. The nonlinear capacitance $C_d(u_d)$ corresponds to the dy-

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namic capacity of the domain assuming a fully depleted domain wall. Thus, we get [2, 3]:

$$C_d = A \sqrt{\frac{en_0 \varepsilon}{2} \frac{1}{\sqrt{u_d}}}, \qquad (1)$$

where A is the cross-sectional area of the sample, e is the electronic charge, n_0 is the carrier concentration and ε is the permittivity of GaAs.

 $i_d(u_d)$ given by an empirical formula represents the dynamic currentvoltage characteristics of the domain. To determine $C_d(u_d)$ and $i_d(u_d)$ at microwave frequencies by measurements is not possible as yet. Results published only for 0,3 mm long samples [6] — are somewhat contradictory to the simplest lumped parameter models [7]. A new method to get the function $i_d(u_d)$ is proposed below.

As it has been mentioned, the position of the switch S depends upon the presence of the domain in the TED. Therefore its operation is controlled by the following conditions:

- 1. The switch takes up position 2 if $u_0 \ge U_{th}$, where U_{th} is the threshold voltage,
- 2. the switch takes up position 1 if $u_0 + u_d < U_s$, where U_s is the sustaining voltage,

or $t \ge t_0 + T_t$, where t_0 is the time at starting the domain and T_t is the transit time.

The lowest possible value of U_s is determined from the implicit formula

$$\left|\frac{di_0}{du_0}\right|_{u_0+u_d=U_s} = \left|\frac{di_d}{du_d}\right|_{u_0+u_d=U_s}$$
(2)

Our actual investigations have been concerned with a modified controlling condition $u_d < U_{sd}$ where U_{sd} is a properly chosen (nearly zero) sustaining domain voltage. It seems to be more exact from physical point of view, but reduces the number of free parameters and makes the correct fitting of the model to a particular device difficult.

The transit time T_t is considered to be constant and measured under ohmic load.

 C_p is the package capacitance.

The proposed model suits to describe

- the finite time of the domain formation and that of the transient response, taking the delaying effect of the accumulated charge into account;

- the vanishing of the domain due to loss of voltage (quenched domain mode) and its dissolution at the anode (transit time mode) as well.

The above described TED model can be used in any particular imbedding circuit. In order to analyse the resulting nonlinear circuit the state equation can be written as follows:

$$\dot{\mathbf{y}} = \mathbf{A}_{1,2}(\mathbf{y}) \, \mathbf{y} + \mathbf{b}_{1,2}(\mathbf{y}, \mathbf{U}_B) \tag{3}$$

where y is the column vector of the state variables, \dot{y} is its time-derivative. A₁ and A₂ are the state matrices while \mathbf{b}_1 and \mathbf{b}_2 are the "constant" vectors depending on the switch position.

As it is obvious from Equ. (3), two alternating initial value problems are to be solved with respect to both positions of the switch S. To assure the continuity of the voltage at the part of the TED model we may assume that in position I

$$u_{0}^{\left(2
ight) }\left(t
ight) =u_{0}^{\left(1
ight) }\left(t
ight) \qquad ext{and}\ u_{d}\left(t
ight) =0$$

while in position 2

$$u_0^{(1)}(t) = u_0^{(2)}(t) + u_d(t)$$

are satisfied formally at any moment. In this way the TED voltage does not change by any change-over of the switch.

The total charge contained by the "activated" circuit after the changeover $2 \rightarrow 1$ of the switch differs from that just before the change-over. But the arbitrarily neglected charge practically does not alter the power in the circuit in a remarkable manner.

When the domain disappears and therefore the switch turns over from $2 \rightarrow l$ due to the transit time but the voltage is still higher than U_{th} the next switching to the position 2 may be artificially delayed by an estimated domain annihilation time.

For the particular example in Fig. 1, Equ. (3) is, in particular:

1. without domain

$$\dot{\mathbf{y}} = \mathbf{A}_{1} \, \mathbf{y} + \mathbf{b}_{1} \qquad \mathbf{y} = \begin{bmatrix} u_{0} \\ i_{L} \end{bmatrix}$$
(3a)
$$\mathbf{A}_{1} = \begin{bmatrix} -\frac{1}{(C_{0} + C)R} & -\frac{1}{C_{0} + C} \\ \frac{1}{L} & 0 \end{bmatrix}$$
$$\mathbf{b}_{1} = \begin{bmatrix} -\frac{i_{0}(u_{0})}{C_{0} + C} + \frac{U_{B}}{(C_{0} + C)R} + \frac{C}{C_{0} + C} \frac{dU_{B}}{dt} \\ -\frac{U_{B}}{L} \end{bmatrix}$$

2. with domain

$$\dot{\mathbf{y}} = \mathbf{A}_{2} \mathbf{y} + \mathbf{b}_{2} \qquad \mathbf{y} = \begin{bmatrix} u_{0} \\ i_{L} \\ u_{d} \end{bmatrix}$$
(3b)
$$\mathbf{A}_{2} = \begin{bmatrix} -\frac{C_{d}(u_{d})}{\Delta R} & -\frac{C_{d}(u_{d})}{\Delta} & -\frac{C_{d}(u_{d})}{\Delta R} \\ \frac{1}{L} & 0 & \frac{1}{L} \\ -\frac{C_{0}}{\Delta R} & -\frac{C_{0}}{\Delta} & -\frac{C_{0}}{\Delta R} \end{bmatrix}$$
$$\mathbf{b}_{2} = \begin{bmatrix} \frac{C_{d}(u_{d})}{\Delta R} & U_{B} + \frac{C}{\Delta} \left[i_{d}(u_{d}) - i_{0}(u_{0}) + C_{d}(u_{d}) \frac{dU_{B}}{dt} \right] - \frac{i_{0}(u_{0})C_{d}(u_{d})}{\Delta} \\ -\frac{U_{B}}{L} \\ \frac{C_{0}}{\Delta R} & U_{B} + \frac{C}{\Delta} \left[i_{0}(u_{0}) - i_{d}(u_{d}) + C_{0} \frac{dU_{B}}{dt} \right] - \frac{i_{d}(u_{d})C_{0}}{\Delta} \end{bmatrix}$$

where

$$\Delta = C_0 C_d (u_d) + CC_0 + CC_d (u_d).$$

Qualitative operation of the TEO computer model

At the beginning, to avoid unnecessary contradictions, a full zero initial condition is chosen. The bias voltage grows continuously from zero up to its final value U_B . Since the voltage is low, at the beginning the switch takes up position 1. In this state Equ. 3a is solved by the Hamming-procedure [8]. When u_0 reaches U_{th} the switch turns to position 2 and the RC_d block representing the domain becomes activated.

Providing some particular sets of parameters of the passive circuit, the domain arrives harmless at the anode and there dissolves (transit time mode, delayed domain mode). This dissolution is represented by the change-over $2 \rightarrow 1$ of the switch S.

If the domain vanishes while in transit because the TED voltage drops below the sustaining value U_s , the switch changes its position as well (quenched domain mode).

As it has been mentioned the solution is composed of a series of individual stages with respect to each change-over of the switch. Each of them is a solution of an individual initial-value problem (*Cauchy-problem*). Their coupling is

governed by the above mentioned rules. The flow chart of the total program is given in Fig. 2.

As it has been mentioned before, the drifting of domains through the sample is represented by the position of the switch. There are, however, transient states where the voltage of the TED oscillates very fast between U_s and U_{th} . These "abortive" domains are generated because of the zero domain



Fig. 2. Flow chart of the program

falltime, or may be attributed to the arbitrary determination of U_s on the basis of Equ. (2).

Nevertheless the computed time-functions of the state variables shown in Fig. 3 are similar to some recorded curves [9, 10] if the average of the ripple of the abortive domains is taken.

The complete computer program performs the *Fourier* analysis of the obtained periodical time functions after having established the cycle time, counting the switch change-overs, the power, efficiency and diode impedance on the required harmonics.



Fig. 3. Calculated state variables vs. time in quenched domain mode

The function $i_d(u_d)$ may be obtained by the following method. The time averages of current and voltage define a point of the D.C. characteristics on the TED. Varying U_B the whole D.C. characteristics can be determined. Inverting this process by an iterative chain, a very important feature appears. The hard-to-measure dynamic characteristics $i_d(u_d)$ can be determined from the D.C. measurements on the oscillating diode.

Research is being done to extend the validity of the model to other modes including the hybrid one, to simplify reckoning with the finite domain falltime, the effects of the inhomogeneous impurity concentration and of the contact region. Examples with different cavities and loads (including the analysis of logic circuits) are being computed.

The operational frequency-range of the different modes, the dependence of the output parameters on the operating frequency of the TED and on the passive circuit parameters are hoped to become available. Thus, by means of the suggested self-consistent lumped model the optimisation of circuits containing TED-s is likely to be feasible.

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Summary

A lumped non-linear model of transferred electron devices is proposed to simulate transit time and quenched domain mode oscillators in a self-consistent manner. A procedure to analyze such a circuit and some remarks on the numerical results are given.

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