

Conductive Paths and Connections on Polymer Substrates for Structural Electronics

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Abstract

Structural Electronics is a modern, still developing technique of manufacturing electrical circuit merged with a mechanical construction of device. This approach opens new possibilities like higher components density, cost effective short series and prototypes or manufacturing of electronics in almost any place like third world countries or space station. In this paper, a novel method of fully printed electronic devices manufacturing is presented. Use of specially prepared paste with silver nanoparticles and microflakes, whose manufacturing procedure is described, as well as sintering with near infrared, continuous wave laser allows to get less than 35 mΩ electrical resistivity of a 1206 SMD resistor single joint. Different 3D printed substrates were tested as well as mechanical and electrical properties of joints were measured. Finally, a simple circuit with LEDs is manufactured, demonstrating the practical used of the presented technique.

Keywords

structural electronics, laser sintering, additive manufacturing, nanosilver conductive paste

1 Introduction

Structural electronics is an extension of printed circuit boards (PCB) which are base of every electronic device. PCB can be found in every factory, most vehicles, household goods or even in the majority of people pocket. Due to its popularity, recent researches were focused on increasing efficiency and reducing costs [1, 2]. Despite the first patents of manufacturing PCB was submitted almost century ago [3], there are still issues that need to develop like prototyping and short-run production methods or environment-friendly manufacturing [4]. These goals perfectly meet structural electronics approach. It is true that PCB prototyping methods have been developed, but existing solutions are based on subtractive methods like CNC machining [5] or laser engraving [6] and what is more they cannot be used in multilayer applications.

The guiding idea of structural electronics is integrating electrical components like resistors, capacitors, integrated circuits etc. with mechanical parts of device (for example casings or objects body). Electronics parts and traces can be conformally placed on the surface or embedded inside the volume of device, what can be done by additive manufacturing methods. 3D printing is a group of additive techniques created especially for prototyping

mechanical parts, thus they meet perfectly set requirements. The development of these methods leads to using their not only in design offices but even final products can be made this way [7]. Today 3D printed products can be found in many industries: from aerospace [8], through automotive and medicine [9] to civil engineer [10] and gastronomy [11, 12]. No wonder then attempts of 3D printed electronics were made. They are two common approaches to this issue. First one is building a non-conductive structure with channels in place of paths and then, after printing filling them with room temperature liquid alloy, like Galinstan [13, 14]. Second one presents alternately printing of structural materials and conductive paths (usually polymer composite like isotropic conductive adhesive or conductive ink) in every layer [15–17]. Both of these solutions have disadvantages: conductivity of used materials (liquid metal and conductive inks/adhesives) is significantly lower (about $2 \cdot 10^6$ S/m [17, 18]) then the conductivity of widely used in electronics metal- copper. What is more polymer composites are unstable in temperature over 200 °C [19] which can be excited during a 3D printing process. Another drawback of a liquid alloy is a minimal adhesive force between electronic components

and a possibility of leaking metal out especially near connectors or switches. For these reasons, it was decided to develop a different type of material based on silver nanoparticles.

Silver nanoparticles (Ag NP) are gaining more and more popularity as a base of modern pastes for electronic joints [20–22]. The biggest advantage of them is fact that sintering temperature (usually about 250–300 °C) is much lower than the maximum service temperature (even over 900 °C [20]). This unusual property is a result of using nanomaterial: melting point of nanoparticles (paste before sintering) is significantly lower than melting point of bulk material (joint after sintering) [23–25]. What is more, silver is metal with the lowest electrical resistivity ($1.59 \cdot 10^{-8} \Omega \cdot m$ [16]), thus Ag NP based paste is excellent material also for temperature stable electronic circuits. In this paper process of preparing Ag NP based paste and method of manufacturing electronic circuit on 3D printed substrates from that paste is presented. Also a three different substrate materials are tested and properties of joints with an electrical components are verificate.

2 Materials and Methods

Production of structural electronics requires developing of new manufacturing methods. Presented solution is based on additive manufacturing extended by thick-film technology and surface-mount devices. Established fabrication process consists of six steps presented on the Fig. 1.

The first step is printing substrate plate by Fused Deposition Modelling (FDM) one of additive manufacturing technique. Groups of materials which can be 3D printed is limited and what is more, substrates usually used in electronics (like FR4, G10 or Teflon) cannot be additively manufactured. Thus in this stage three polymer materials was tested: poly(lactic acid) (PLA) – the most popular material in both industrial and desktop Fused Deposition Modelling (FDM) printers, ULTEM – polymer with have one of the highest performance from available material and HP 3D High Reusability PA 12 (HPPA) – a material for Multi Jet Fusion (MJF) printer, which is one of the most precise system commercially available on the market, both (material and printer) developed by Hewlett-Packard. From these materials substrate plates with dimension of $37.5 \times 25 \times 2.5$ mm and 100 % infill was printed.

On the top of each plate a circuit pattern was made by screen printing of a non-commercial conductive paste designed and manufactured on the Faculty of Mechatronics

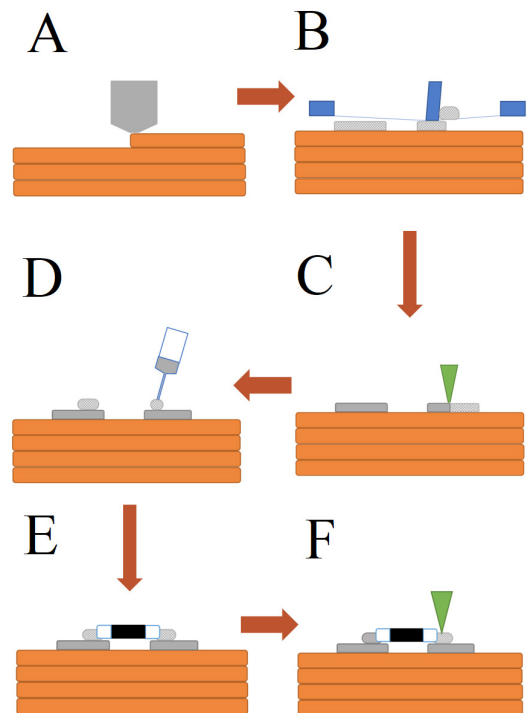


Fig. 1 Scheme of a developed manufacturing process: A. 3D printing of a polymer substrate; B. The circuit pattern screen printing; C. Laser sintering of printed paste; D. Dispensing paste on pads; E. Placing a SMD components; F. Laser sintering of connections.

Warsaw University of Technology in Poland. Conductive component of this material is mixture of silver nanoparticles (with size 30.5 ± 5.4 nm) and silver microflakes of average size 3 μm and thickness 100 nm. Silver nanoparticles were coated with salts of palmitic acid to prevent from room temperature agglomeration. To achieve rheology parameters suitable for screen printing, silver powders was suspended in toluene and dispersed in the organic vehicle. After testing it was decided to choose 8% wt. solution of poly(methyl methacrylate) (with average molar mass M_w 350,000, delivered by Sigma-Aldrich) in [2-(2-Butoxyethoxy)ethyl] acetate. More details about this paste can be found in [26] and final composition is: silver nanopowders (14 % wt.), silver microflakes (59 % wt.), organic vehicle (20 % wt.), organic residues (7 % wt.). Then described paste was screen printed on top of each plate by using polyethylene screen with 175 mesh.

Paste after print needs to be heat cured and usually it is done by heating in oven at 300 °C. In this case such high temperature can damage substrate plates, thus, a novel method base on laser scanning was developed. The laser diode at 808 nm wavelength was mounted on a 3axies cnc machine and used as a local heat source to sinter a

paste layer. Density of power in 200 μm spot was about 250 W/mm^2 and scanning was performed with a speed of 1000 mm/s and 100 μm hatch distance. Using of laser beam allows to achieve a high temperature gradient during a heat treatment and obtain a fully sintered silver layer without significantly damaging the substrate. The next step was assembly of electrical components- SMD 1206 resistors. Silver paste described above was used as binding agent and dispensed from syringe on pads. Then resistors was placed on wet paste, which finally was laser sintered to make electrical and mechanical connections between pads and components.

On obtained joints to types of measurements were taken: electrical and mechanical. Resistance of single joint was measured by a 4-wire method with the Escort 3145A multimeter. For each substrate material 8 samples was prepared, thus 16 joints was tested. Then mechanical properties of manufactured joints were verified in the shear test with registered force needed to break a connection. In this test 8 samples for each substrate was measured.

3 Results

For each substrate material average joint resistance and standard deviation of it are shown in a Fig. 2A. We can see, that for joints made on a ULTEM substrate have the highest standard deviation, over 50 % of average ($34 \text{ m}\Omega \pm 18 \text{ m}\Omega$). That means that these results are uncertain and for this polymer a sintering process are not so stable like for PLA or HPPA. Opposite outcomes obtained for PLA: an average resistance was relatively high, but a standard deviation of it is the lowest for these measurements ($60 \text{ m}\Omega \pm 11 \text{ m}\Omega$). Properties of joints made on a HPPA are between PLA and ULTEM: noticed $42 \text{ m}\Omega \pm 16 \text{ m}\Omega$.

Data obtained in shear test are presented in a Fig. 2B. We observe that the most durable connection was made on a PLA and average force was $50 \text{ N} \pm 12 \text{ N}$. On the opposite side was results observed for a ULTEM base circuits: $14.4 \text{ N} \pm 2.6 \text{ N}$. Attributes of a HPPA as a substrate was again between rest two polymers: recorded average force 28.2 N with 7.8 N standard deviation.

Collected data show us that none of tested substrate material are significantly better than others: all of them have advantages in one field and cons in others. On the other hand in structural electronics mechanical durability aren't so crucial because all electrical components are surrounded by structural material. However, connections on ULTEM substrate still have big range of obtained resistance, what can be troubling in some applications. Another important attribute of tested materials is availability

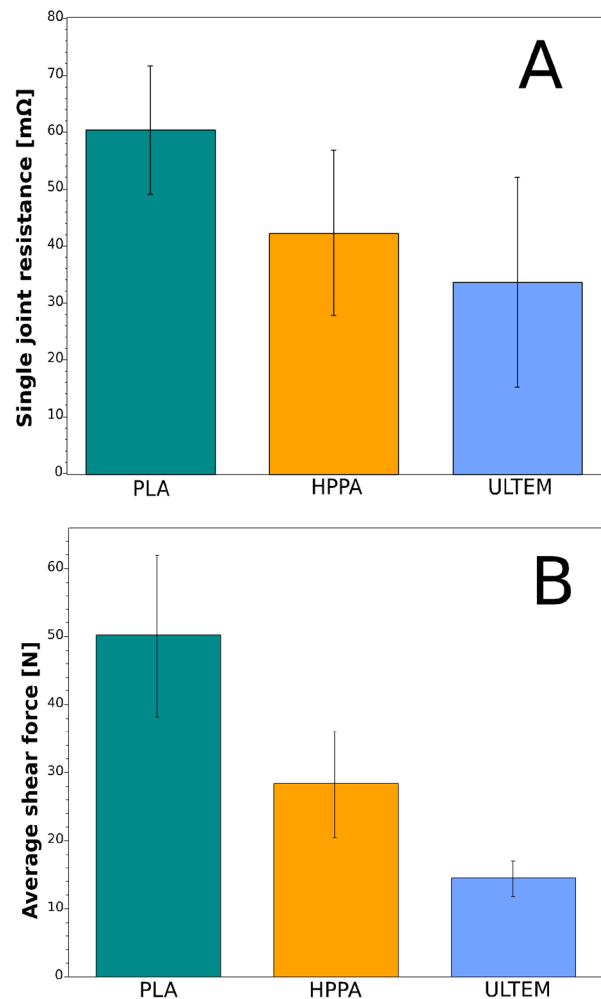


Fig. 2 Electrical (A) and mechanical (B) properties of the 1206 SMD resistor joints made by laser sintering of nanosilver paste on different polymer substrates.

of device which can print from them: using ULTEM or HPPA require specific printer, manufactured only by individual companies. Only a PLA polymer can be processed by countless and constantly increasing number of printers.

To demonstrate the possibility of practical use of this technology, we build simple hybrid circuit with a SMD LEDs on PLA substrate plate (shown in Fig. 3).

4 Conclusions

The technique of a fully additive manufacturing of electronic circuit was presented. Conductive paste based on silver nanoparticles and microflakes was developed, which was used to produce pads, paths and joints with electrical components. Three different substrate materials was tested and dissimilar properties in electrical and mechanical field was observed: highest mechanical durability was noticed for circuits made on PLA but at the same time

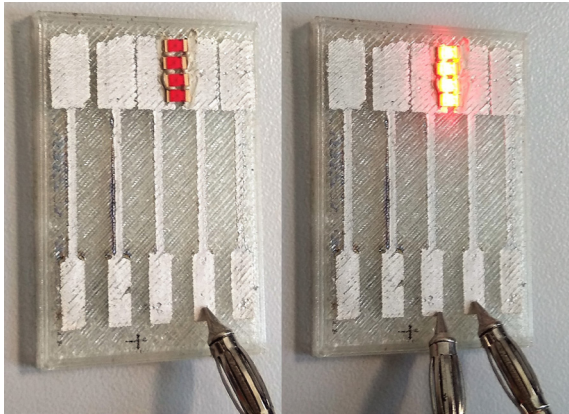


Fig. 3 Circuit manufactured on PLA 3D printed substrate by laser sintering of SMD LEDs to screen-printed nanosilver paste pads cured by laser.

single joint electrical resistance was 50 % or more higher than for circuits based on others polymers. Nevertheless PLA is also easily accessible and can be processed by popular desktop 3D printers, thus we think that this material is the most suitable for further research.

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